

E²SWITCH Final Workshop

10 November 2017 — imec, Belgium

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| 9h00 | Welcome and introduction to the workshop | Adrian Ionescu » EPFL |
| 9h10 | Physics an performance of III-V nanowire heterojunction TFETs including phonon and impurity band tails: An atomistic mode space NEGF quantum transport study | Aryan Azfalian » Guest – TSMC |
| 9h40 | III-V nanowire TFETs | Lars-Erik Wernersson » Lund |
| 10h10 | Energy filters and sub-unity body factors as boosters for better energy efficiency on advanced CMOS platforms' | Adrian Ionescu » EPFL |
| 10h40 | Break | |
| 11h00 | Tunnel-FET: bridging the gap between prediction and experiment through calibration | Anne Verhulst » Guest – imec |
| 11h30 | Complementary III-V heterojunction TFETs | Kirsten Moselund » IBM |
| 12h00 | Group IV semiconductor TFETs | Qing-Tai Zhao » JUELICH |
| 12h30 | Lunch | |
| 13h45 | Modeling the effect of non-idealities on III-V/Si and All-III-V TFETs | Saurabh Sant » ETHZ |
| 14h15 | Digital evaluation of TFETs in scaled nodes | Dmitry Yakimets Marie Garcia Bardon » imec |
| 14h45 | Full-quantum modeling of Si/SiGe/III-V Tunnel-FETs architectures | Elena Gnani » IUNET |
| 15h05 | Simulation of digital and analog/mixed-signal circuits employing Tunnel-FETs | Pierpaolo Palestri » IUNET |
| 15h25 | Break | |
| 15h45 | TFET based on 2-D semiconductors: a modeling perspective | Mathieu Lusier » ETHZ |
| 16h15 | Building transistors with 2D for back-end applications | Iuliana Radu » imec |



The research leading to these results has received funding from the European Community's Seventh Framework Programme under grant agreement No. 619509 (Project E²SWITCH).